Refine Search

Search Results -

Terms	Documents
L15 and latency.ab.	6

Database:
```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search:
```
L17
```

[Refine Search]

[Recall Text ⬍] [Clear] [Interrupt]

Search History

DATE: Sunday, September 30, 2007   **Purge Queries**   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=NO; OP=OR		
L17	L15 and latency.ab.	6	L17
L16	L15 and (insert or add).ab. adj instruction.ab.	0	L16
L15	L14 and L13 and L12	60	L15
L14	(712/205 |712/206 |712/207 |712/208 |712/226 |712/233 |712/234 |712/235 |712/236 |712/237 |712/238 |712/239 |712/240 |712/241).ccls.	2750	L14
L13	(711/118 |711/119 |711/120 |711/121 |711/122 |711/123 |711/124 |711/125 |711/126 |711/127 |711/128 |711/129 |711/130 |711/131 |711/132 |711/133 |711/134 |711/135 |711/136 |711/137 |711/138 |711/139 |711/140 |711/204).ccls.	5486	L13
L12	L8 or L9	1275	L12
L11	L10 or L9	317	L11
L10	L9 and compiler	145	L10
L9	L8 and speculative	317	L9

L8	L7 or L6 or L5	1275	L8
L7	L6 and insert	298	L7
L6	L2 and latency	1269	L6
L5	L2 and (counter ADJ thread)	22	L5
L4	L2 and (helper ADJ thread)	0	L4
L3	L2 and speculative	444	L3
L2	L1 and counter	2632	L2
L1	((cache) adj (miss or misses))	6182	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L19 and latency.ab.	16

Database:
```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search:
```
L20
```

[Refine Search]

[Recall Text ⬍]   [Clear]   [Interrupt]

Search History

DATE: Sunday, September 30, 2007   Purge Queries   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB; PLUR=NO; OP=OR			
L20	L19 and latency.ab.	16	L20
L19	(712/205 |712/206 |712/207 |712/208 |712/226 |712/233 |712/234 |712/235 |712/236 |712/237 |712/238 |712/239 |712/240 |712/241).ccls.	782	L19
L18	(711/118 |711/119 |711/120 |711/121 |711/122 |711/123 |711/124 |711/125 |711/126 |711/127 |711/128 |711/129 |711/130 |711/131 |711/132 |711/133 |711/134 |711/135 |711/136 |711/137 |711/138 |711/139 |711/140 |711/204).ccls.	2268	L18
DB=USPT; PLUR=NO; OP=OR			
L17	L15 and latency.ab.	6	L17
L16	L15 and (insert or add).ab. adj instruction.ab.	0	L16
L15	L14 and L13 and L12	60	L15
L14	(712/205 |712/206 |712/207 |712/208 |712/226 |712/233 |712/234 |712/235 |712/236 |712/237 |712/238 |712/239 |712/240 |712/241).ccls.	2750	L14

L13	(711/118 |711/119 |711/120 |711/121 |711/122 |711/123 |711/124 |711/125 |711/126 |711/127 |711/128 |711/129 |711/130 |711/131 |711/132 |711/133 |711/134 |711/135 |711/136 |711/137 |711/138 |711/139 |711/140 |711/204).ccls.	5486	L13
L12	L8 or L9	1275	L12
L11	L10 or L9	317	L11
L10	L9 and compiler	145	L10
L9	L8 and speculative	317	L9
L8	L7 or L6 or L5	1275	L8
L7	L6 and insert	298	L7
L6	L2 and latency	1269	L6
L5	L2 and (counter ADJ thread)	22	L5
L4	L2 and (helper ADJ thread)	0	L4
L3	L2 and speculative	444	L3
L2	L1 and counter	2632	L2
L1	((cache) adj (miss or misses))	6182	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(718/103).ccls. and L20	0

Database:

```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search:

```
L22
```

[Refine Search]

[Recall Text ⬍]   [Clear]   [Interrupt]

Search History

DATE: Sunday, September 30, 2007   **Purge Queries**   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB; PLUR=NO; OP=OR			
L22	718/103.ccls. and L20	0	L22
DB=USPT; PLUR=NO; OP=OR			
L21	718/103.ccls. and L15	0	L21
DB=PGPB; PLUR=NO; OP=OR			
L20	L19 and latency.ab.	16	L20
L19	(712/205 |712/206 |712/207 |712/208 |712/226 |712/233 |712/234 |712/235 |712/236 |712/237 |712/238 |712/239 |712/240 |712/241).ccls.	782	L19
L18	(711/118 |711/119 |711/120 |711/121 |711/122 |711/123 |711/124 |711/125 |711/126 |711/127 |711/128 |711/129 |711/130 |711/131 |711/132 |711/133 |711/134 |711/135 |711/136 |711/137 |711/138 |711/139 |711/140 |711/204).ccls.	2268	L18
DB=USPT; PLUR=NO; OP=OR			
L17	L15 and latency.ab.	6	L17

L16	L15 and (insert or add).ab. adj instruction.ab.	0	L16
L15	L14 and L13 and L12	60	L15
L14	(712/205 |712/206 |712/207 |712/208 |712/226 |712/233 |712/234 |712/235 |712/236 |712/237 |712/238 |712/239 |712/240 |712/241).ccls.	2750	L14
L13	(711/118 |711/119 |711/120 |711/121 |711/122 |711/123 |711/124 |711/125 |711/126 |711/127 |711/128 |711/129 |711/130 |711/131 |711/132 |711/133 |711/134 |711/135 |711/136 |711/137 |711/138 |711/139 |711/140 |711/204).ccls.	5486	L13
L12	L8 or L9	1275	L12
L11	L10 or L9	317	L11
L10	L9 and compiler	145	L10
L9	L8 and speculative	317	L9
L8	L7 or L6 or L5	1275	L8
L7	L6 and insert	298	L7
L6	L2 and latency	1269	L6
L5	L2 and (counter ADJ thread)	22	L5
L4	L2 and (helper ADJ thread)	0	L4
L3	L2 and speculative	444	L3
L2	L1 and counter	2632	L2
L1	((cache) adj (miss or misses))	6182	L1

END OF SEARCH HISTORY